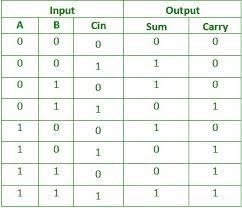
10. Design and implement 3-bit full adder using logisim simulator.

Truth table:



Circuit diagram:

